



X-786 US  
09/757,404

PATENT  
Conf. No.: 6382

IN THE UNITED STATES PATENT OFFICE

Applicant: Steven A. Guccione et al.

Assignee: Xilinx, Inc.

Title: "METHOD AND SYSTEM FOR DEVICE-LEVEL SIMULATION OF A CIRCUIT DESIGN FOR A PROGRAMMABLE LOGIC DEVICE"

Ser. No.: 09/757,404 Filing Date: 01/08/2001

Examiner: Mary C. Hogan Art Unit: 2123

Docket No.: X-786 US Conf. No.: 6382

---

Mail Stop AF  
COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

In response to the Final Office Action dated November 23, 2004, reconsideration and allowance of all claims are respectfully requested in view of the remarks that follow.

**Listing of the Claims** are reflected in the listing of claims, which begins on page 2 of this paper.

**Remarks** begin on page 7 of this paper.

*OK to enter*